



High efficiency transmission masks for EUV interference lithography

S. Brose¹, L. Juschkin², S. Danylyuk¹, H. Kim¹, P. Loosen¹, D. Grützmacher³

¹ Chair for the Technology of Optical Systems, RWTH Aachen University and JARA-Fundamentals of Future Information Technology, Aachen, 52074, Germany,

² Experimental Physics, RWTH Aachen University and JARA-Fundamentals of Future Information Technology, Aachen, 52074, Germany,

³ Peter Grünberg Institut, Forschungszentrum Jülich and JARA-Fundamentals of Future Information Technology, Jülich, 52425, Germany

Abstract

Lithography with an extreme ultraviolet radiation (EUV) is currently the main candidate for next-generation optical lithography [1]. The structuring with EUV light pushes the diffraction resolution limit into sub-10 nm range due to the short wavelength (typically between 10 nm and 15 nm). However, number of technical challenges, such as availability of high power laboratory EUV sources, lifetime of optical components, mask manufacturing and defect control, slow the wide spread implementation of the technique. For interference lithography with EUV radiation one of the main challenges is a manufacturing of low-defect high-efficiency transmission gratings. Due to a very strong absorption of EUV radiation in a matter only a very thin (< 300 nm) free-standing membrane of selected materials can be used for a transmission part of the grating. Standard membrane technology utilizes silicon nitride membranes [2, 3] that are mechanically stable and relatively transparent between 12.4 nm and 16 nm. The presented fabrication process of high-efficiency transmission masks is based on free-standing niobium membranes and demonstrate high contrast not only for in-band EUV (13.5 nm) radiation but also for wavelengths below Si L-absorption edge (12.4 nm) targeted for next-generation lithography.

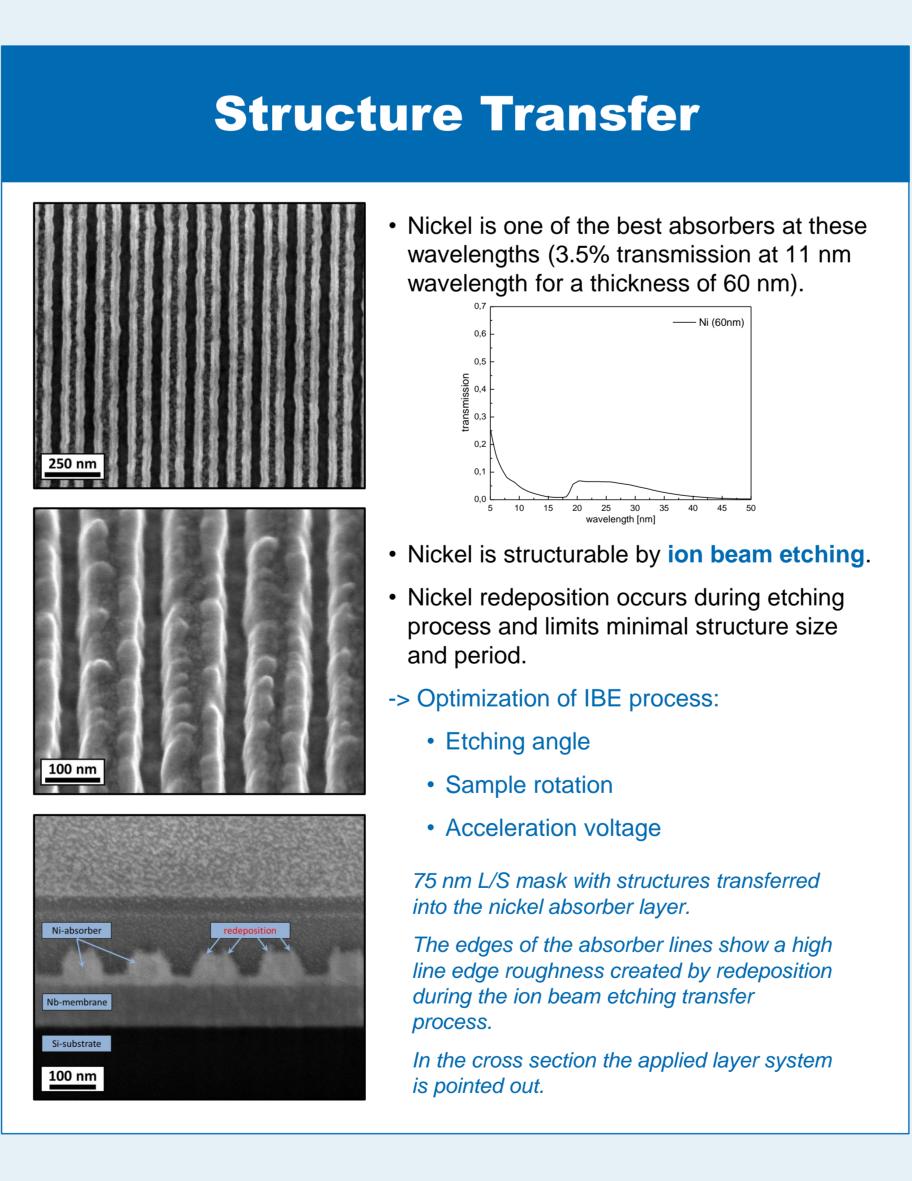
The masks and filters with free standing areas up to 1000 x 1000 µm² and 100 nm to 300 nm membrane thicknesses are shown. Electron beam lithography and ion beam etching of an absorber layer with dense line and dot patterns with sub-50 nm structures is demonstrated [4].

Cross section A-A: e-beam ion beam kOH wet etch wet etch

- 1. Deposition of high quality and homogeneity layers on chemically cleaned silicon to avoid pores and pinholes.
- 2. E-beam lithography of high resolution resists with preceding fine dose selection.
- 3. Structure transfer into absorber layer via ion beam etching.
- 4. Anisotropic wet etching process for silicon (100) substrate removal.

Transmission Measurements Comparison of the — Nb(100nm)/Si₂N₄(10nm) transmission curves of - Si₃N₄(110nm) conventional Si₃N₄ membrane and the developed membrane layer system Nb/Si₃N₄. .io 0.4 The transmission is approximately around 50% even down to wavelength considered for nextgeneration lithography. The measured 0.1 transmittance at 10.9 nm verifies the theoretical data wavelength [nm] Broadband emission ---- without filter spectra of Xe/Ar discharge with 300nm Nb-filter produced plasma EUV source with and without The membrane material serves as a built-in filter for higher wavelengths resulting in a transmitted spectrum with 3.2%bandwidth at 10.9 nm wavelength required for interference lithography utilising achromatic talbot wavelength [nm]

Resist Patterning Electron beam lithography has to be done on Nb/Ni (increased electron backscattering) dense structures over large areas (fill factor ~50%) resist thickness of at least 60 nm is needed for structure transfer (IBE) -> high resolution and contrast e-beam resists 50 nm lines/spaces array in 80 nm thick HSQ negative -> fine dose selection resist. Left inset: overexposed area due to electron backscattering. Right inset: good resolved structures -> optimized development but only on the edge of the 500 x 500 µm² array. process Monte Carlo[©] simulation of electron —— Resist(105nm)/Si [50kV] backscattering on different layer —— Resist(105nm)/Ni(60nm)/Nb(100nm)/Si [50kV] <u>∂</u> 10000 I Resist(105nm)/Ni(60nm)/Nb(100nm) [50kV] systems at 50 kV acceleration 1000 The influence of niobium and nickel is clearly visible in a range of 30 nm up to 2 μm around the injection Structuring the resist directly on the membrane reduces the long range scattering introduced by the radius [µm]



Mask Patterns Overall mask layout. The complete main field size is in this case $1 \times 1 \text{ mm}^2$. The main field is divided into four sub fields to realize different pattern types with just one illumination step. The mask is equipped with a set of alignment markers to allow implementation of a EUV lithography process into existing process chains. 200 μm Close-up view of a rectangular pinhole array. The period is 100 nm and the pinhole diameter 40 nm. Nickel absorber thickness is 60 nm. 200 nm Close-up view of a lines/spaces array with a period of 150 nm, line width is approximately 90 nm. The line edge roughness is +/-5 nm. Nickel absorber thickness is 50 nm. The subjacent niobium membrane is clearly visible. Array of rectangles with dimensions in the µm-range. Nickel absorber thickness is 80 nm. This transmission mask can be applied for the fabrication of nanophotonic components. 2 μm Close-up view of a hexagonal pinhole array. The period is 100 nm and the pinhole diameter 40 nm. Nickel absorber thickness is 60 nm.

Outlook

Approaches for further structure and period reduction:

- Reduction of nickel redeposition.
- Application of selective nickel electroplating deposition (currently under investigation).
- Decreasing the absorber thickness.
- Chrome absorber layer instead of nickel enables implementation of highly selective RIE Cl₂-process.
- Instead of creating amplitude transmission masks it is also possible to create phase shift masks based on niobium with RIE SF₆-process.
- **Increase of membrane field** for higher throughput:
- Optimization of internal stress distribution in niobium membrane.
- Increasing the membrane thickness leads to a stiffer membrane.
- Implementation of a support mesh into the fabrication process.
- Creation of a multi field transmission mask with silicon support structures.
- [1] The international technology roadmap for semiconductors: 2010 update, http://www.itrs.net
- [2] H.H. Solak, C. David, J. Gobrecht, V. Golovkina, F. Cerrina, S.O. Kim, P.F. Nealey, Microelectron. Eng. 2003, 67-68, 56
- [3] P.P. Naulleau, C.H. Cho, E.M. Gullikson, J. Bokor, J. Synchrotron Radiat.
- 2000, 7, 405[4] S. Brose, S. Danylyuk, L. Juschkin, C. Dittberner, K. Bergmann, J. Moers, G. Panaitov, St. Trellenkamp, P. Loosen, D. Grützmacher, Thin Solid
- [5] B.L. Henke, E.M. Gullikson, J.C. Davis, At. Data Nucl. Data Tables 1993, 54 (2), 181

Films 2012, 520, 5080

[6] K. Bergmann, S. Danylyuk, L. Juschkin, J. Appl. Phys. 2009, 106, 073309